

#### Abstract of the Disclosure

A CPU stores, when there is a difference between current channel information and new channel information obtained by a new channel search, the new channel information in a memory while holding a state where the current channel information is registered (contents stored in a non-volatile memory). The CPU displays on a screen the channel information between which the difference is presented using an OSD circuit, and changes the contents of the registration of the current channel information on the basis of the new channel information by a user operation on the screen.